

Fig. 1
(prior art)

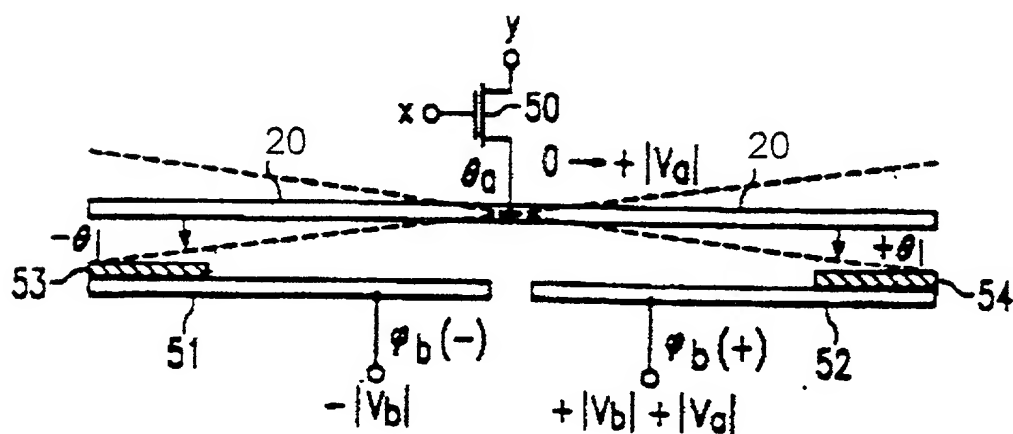


Fig. 2a
(prior art)

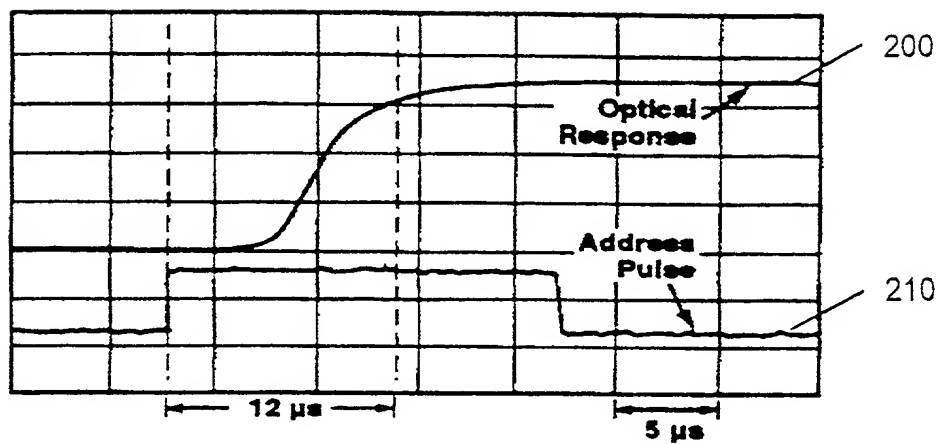


Fig. 2b (prior art)

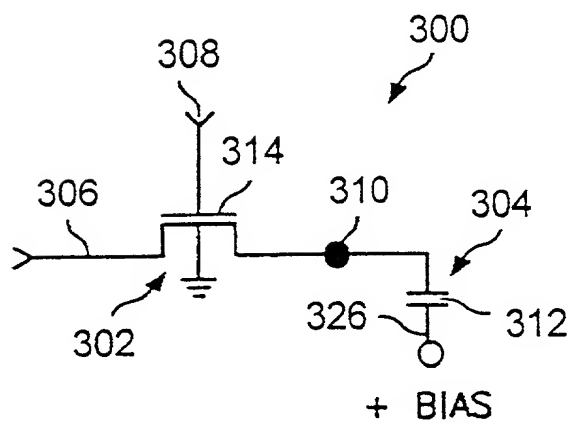


Fig. 4a (prior art)

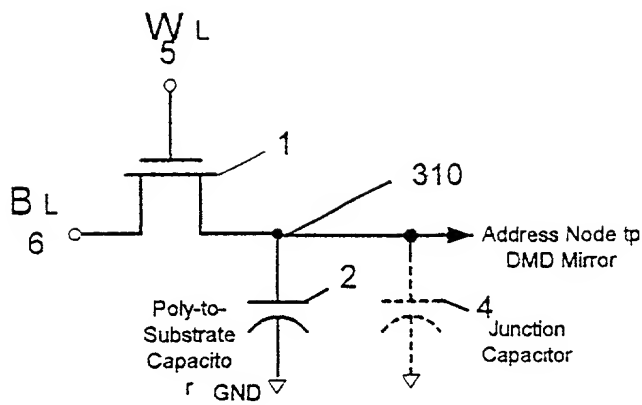


Fig. 5a

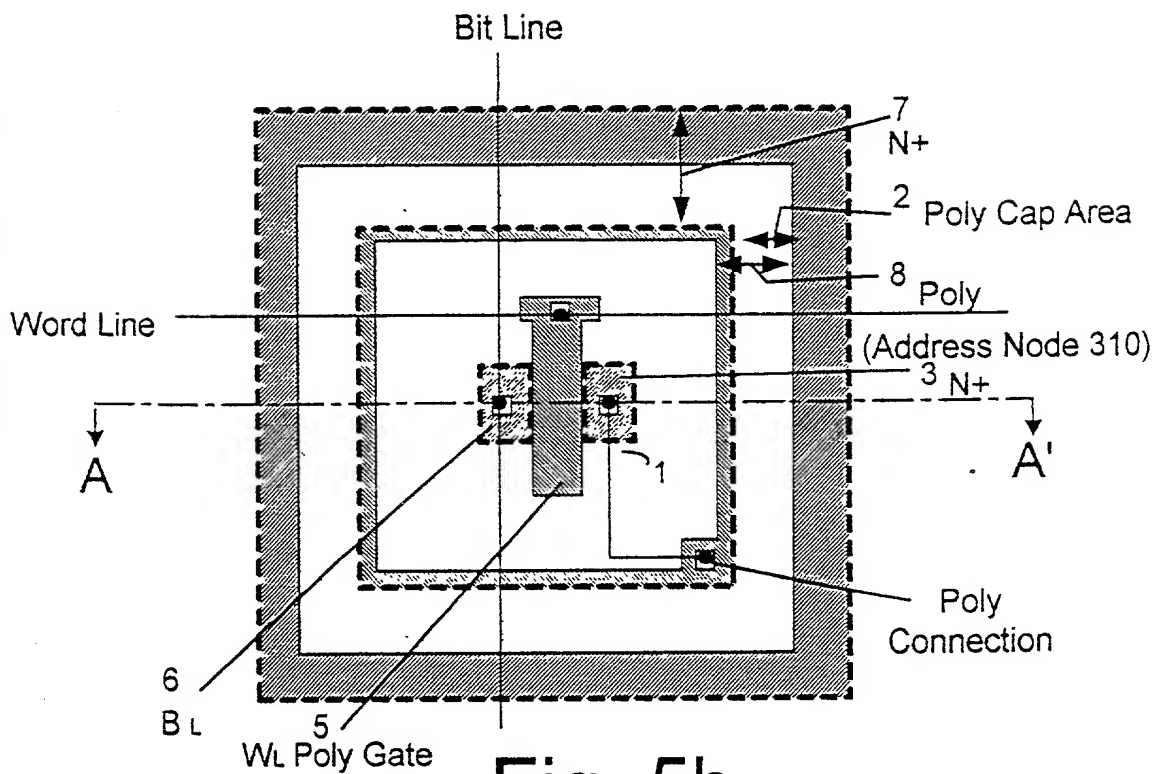


Fig. 5b

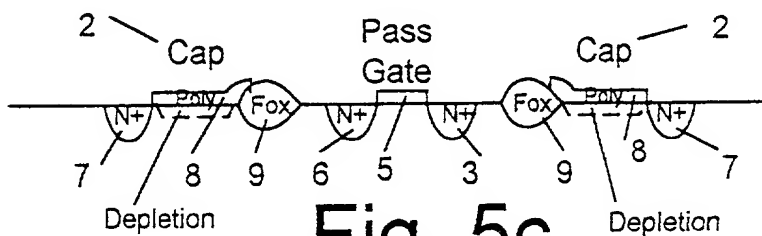


Fig. 5c

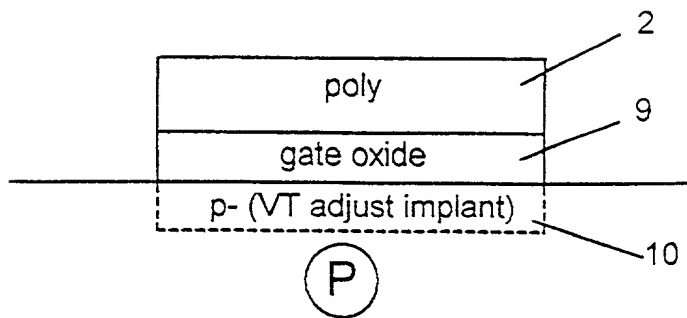


Fig. 6a

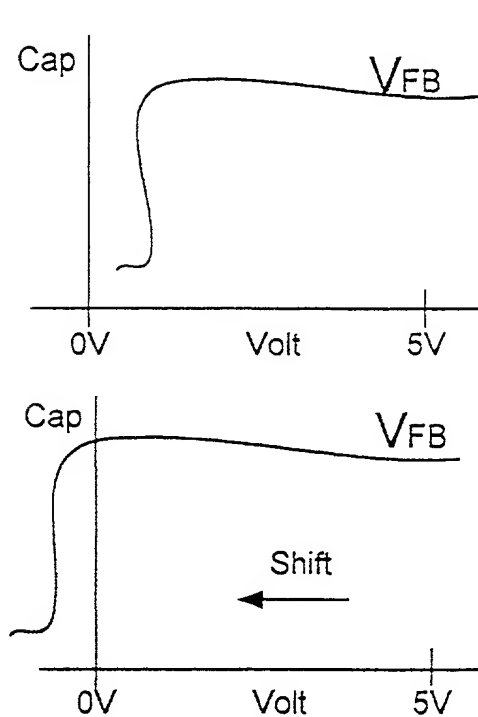


Fig. 6b

FIG. 6a

Fig. 6b

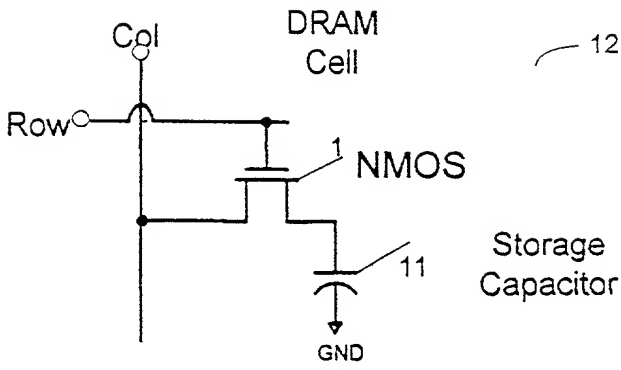


Fig. 7a

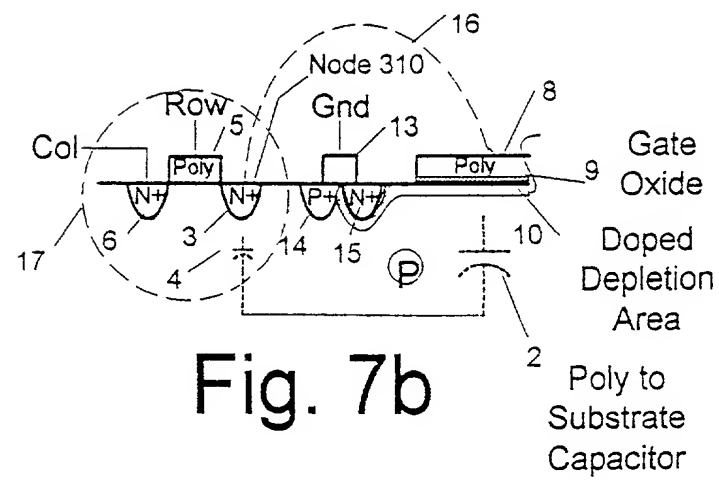


Fig. 7b

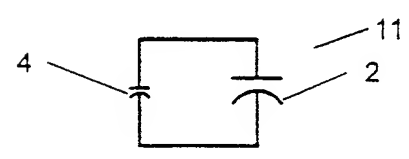
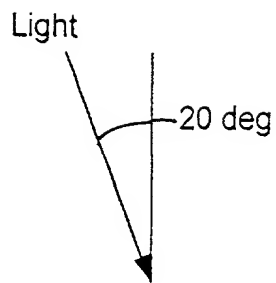
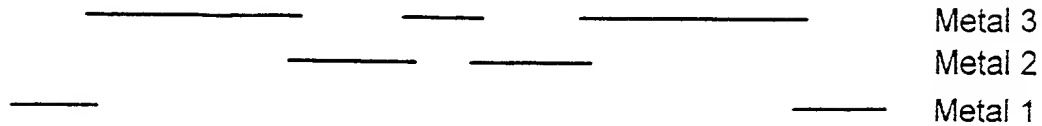


Fig. 7c

FIG. 7a



(Metal coverage of DRAM CELL)



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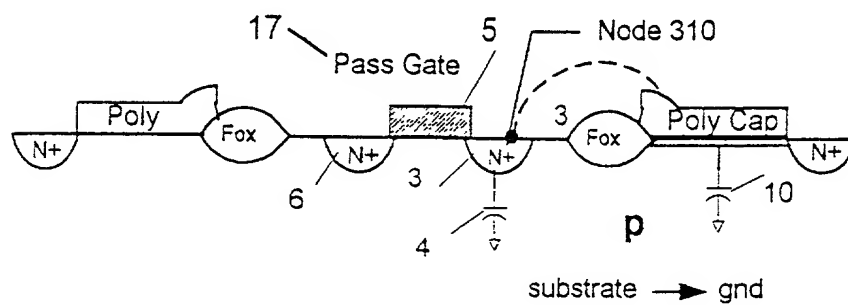


Fig.8A

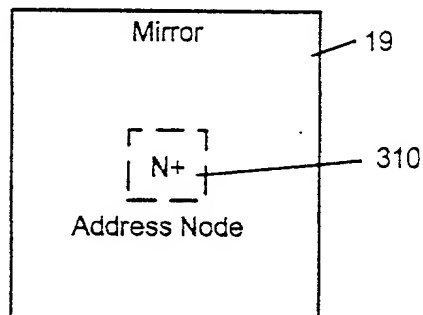


Fig. 8B

Fig. 9a

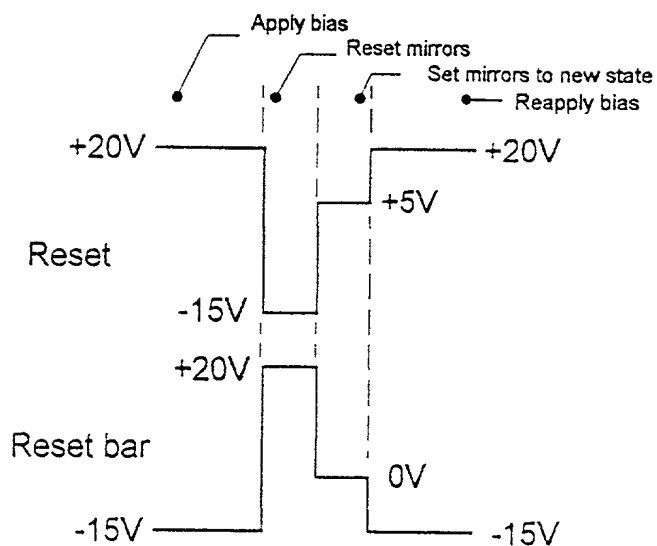


Fig. 9b

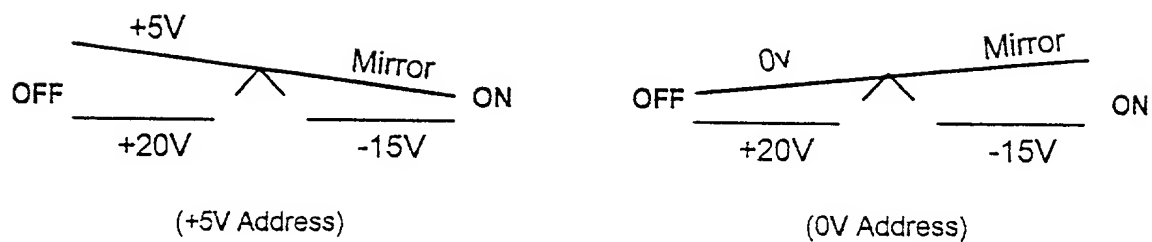


Fig. 9c

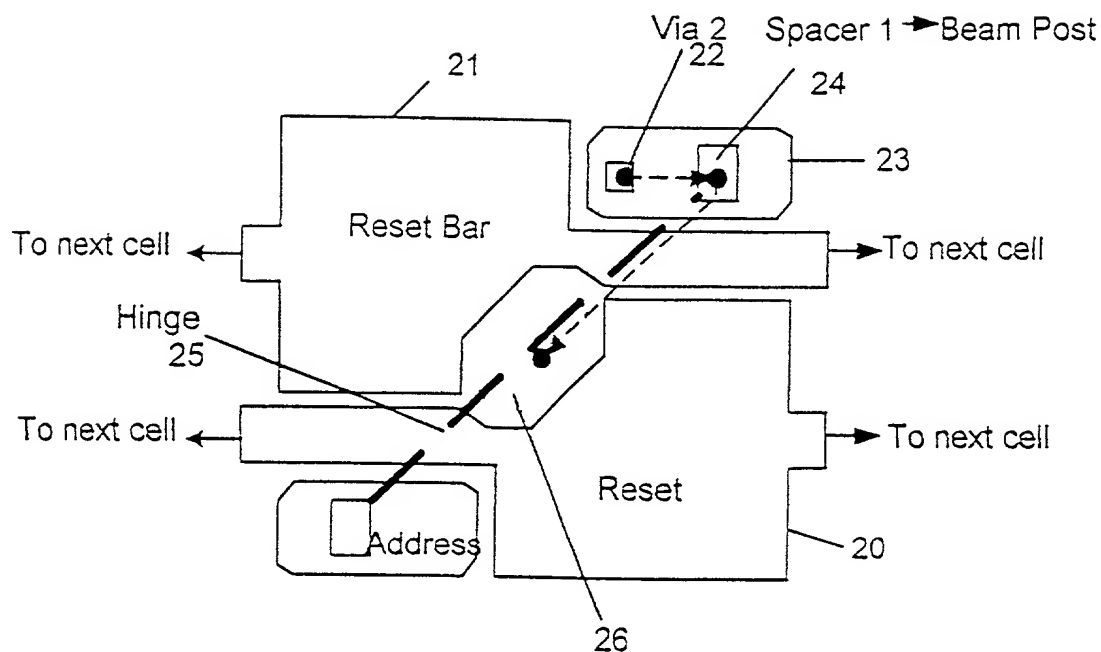


Fig. 10a

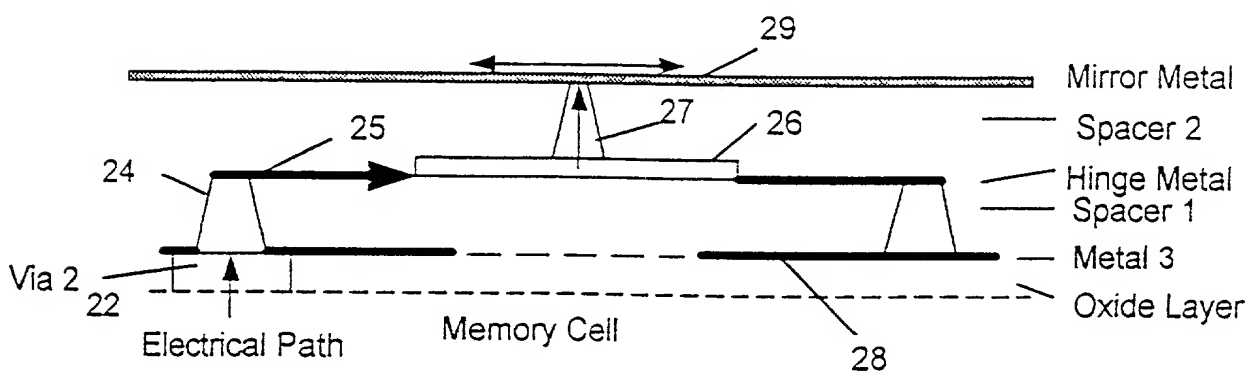


Fig. 10b

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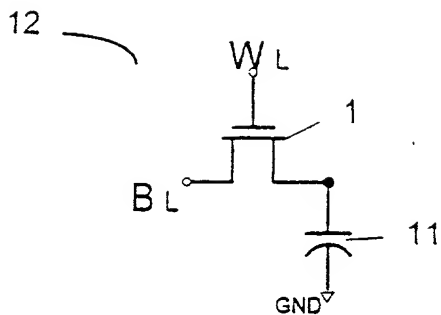


Fig. 11a

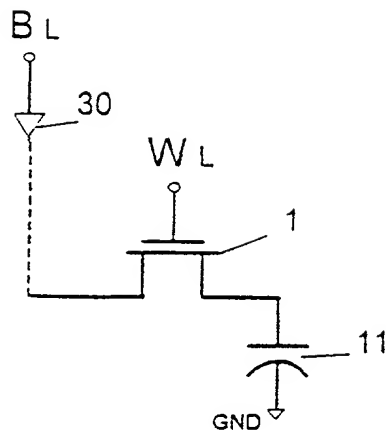


Fig. 11b

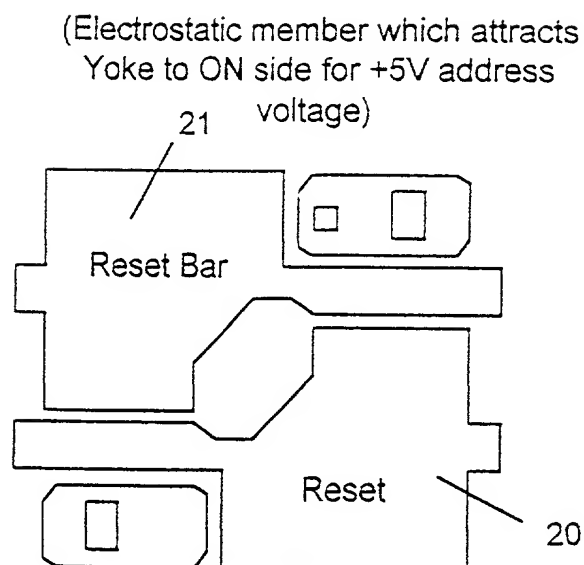


Fig. 11c

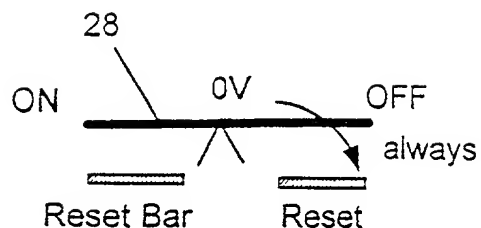


Fig. 11D

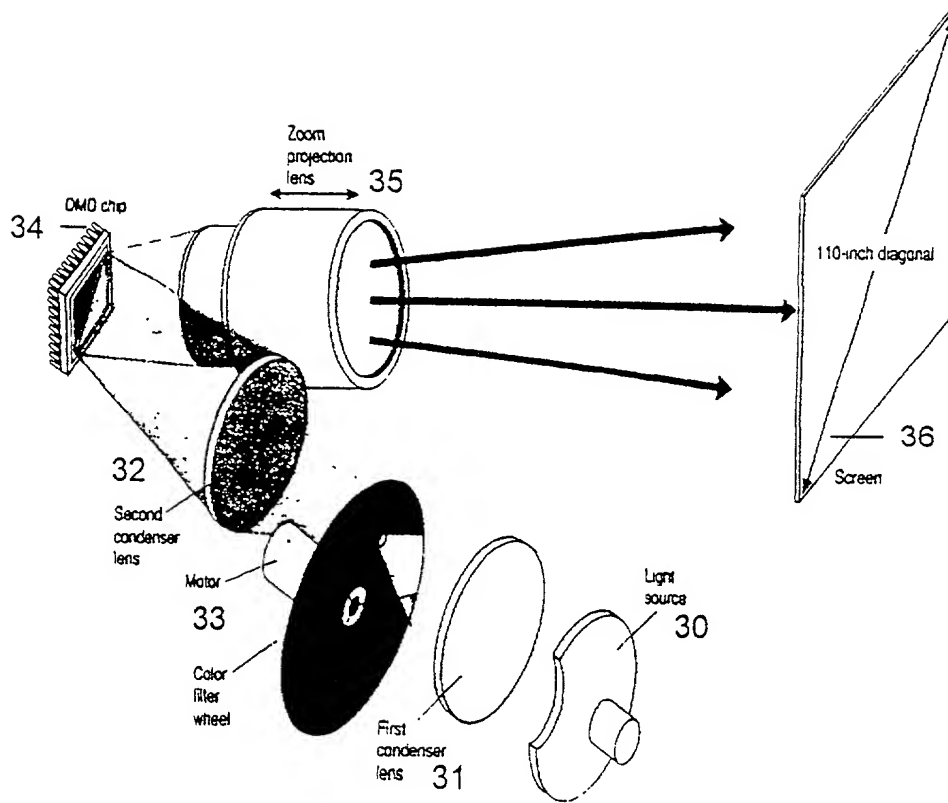


Fig. 12a

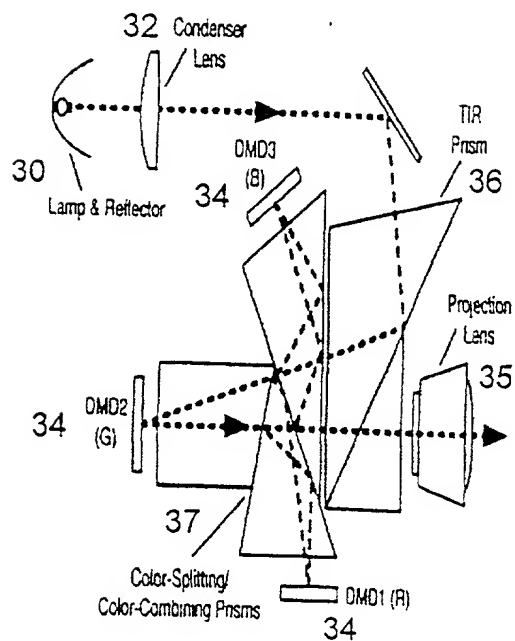


Fig. 12b